100

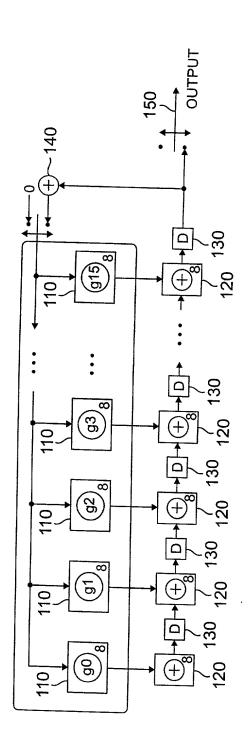


FIG. 1 (PRIOR ART)

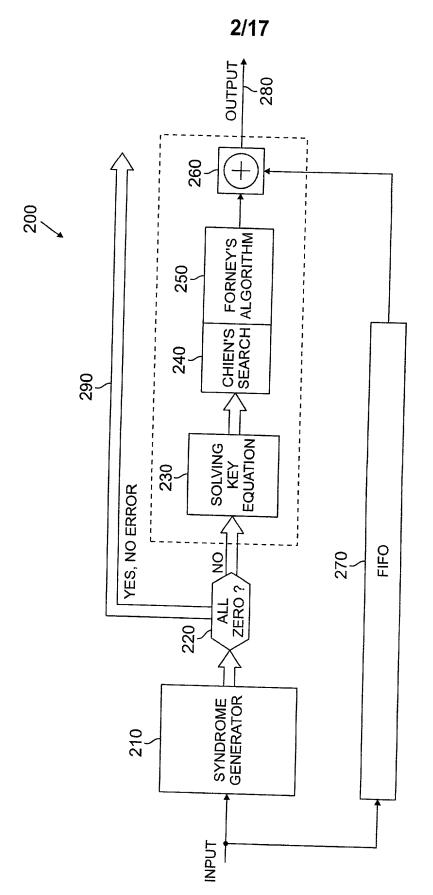
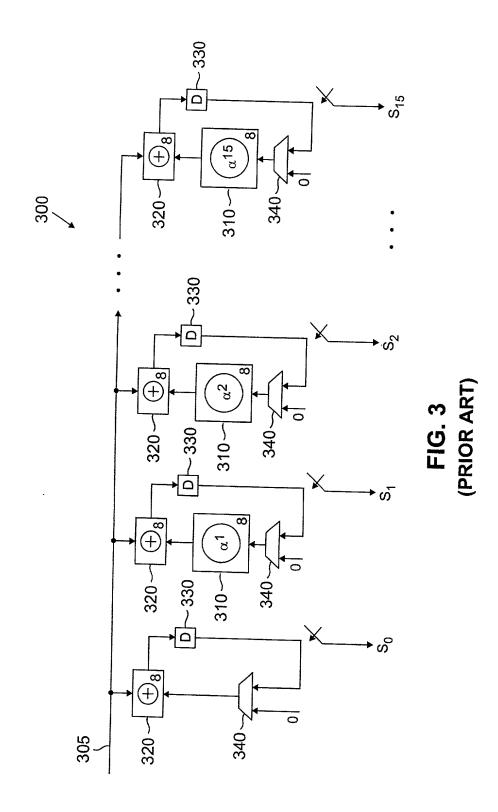


FIG. 2 (PRIOR ART)



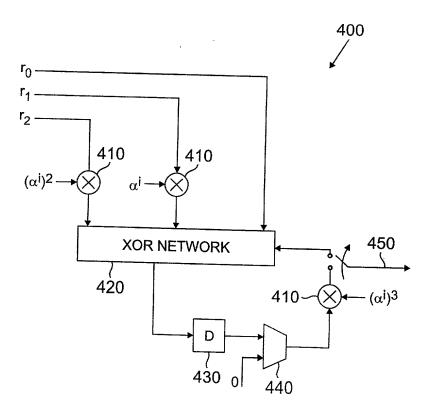
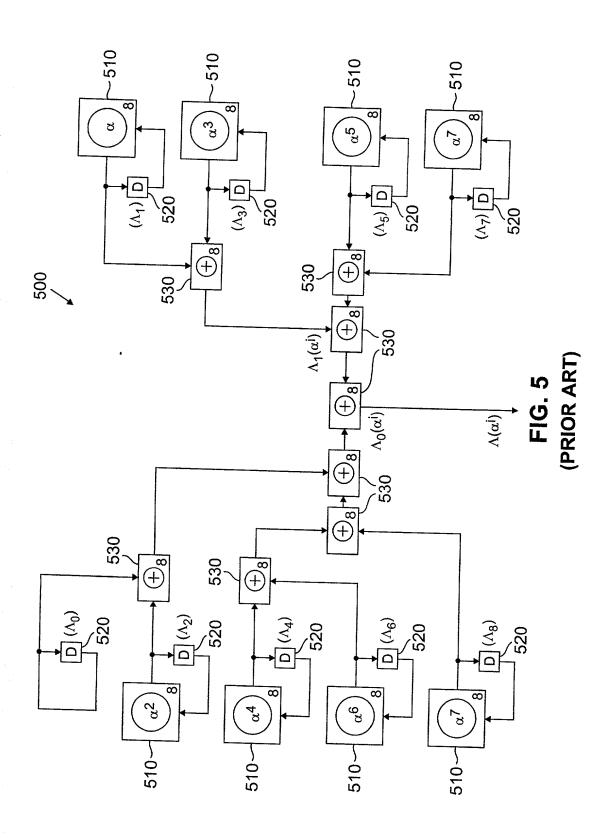


FIG. 4



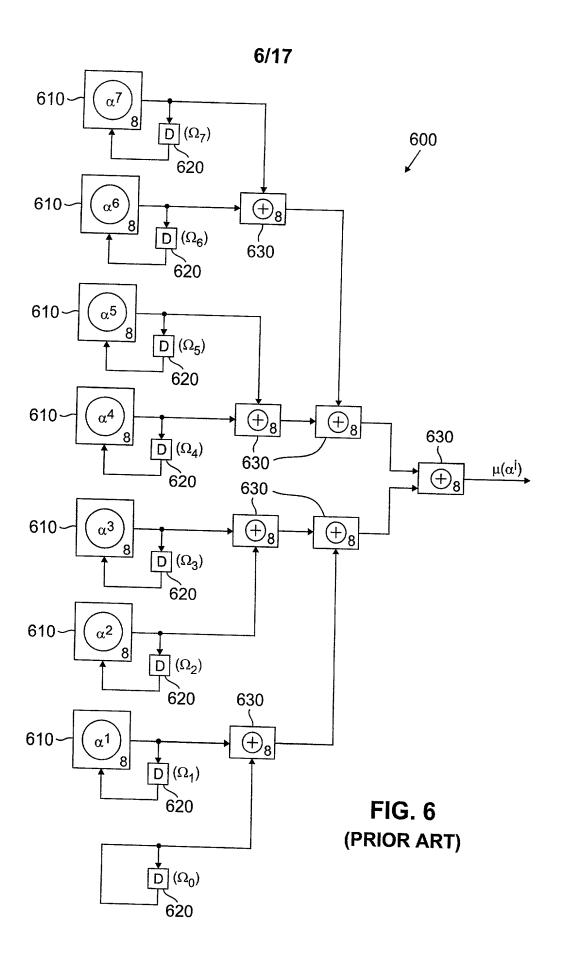
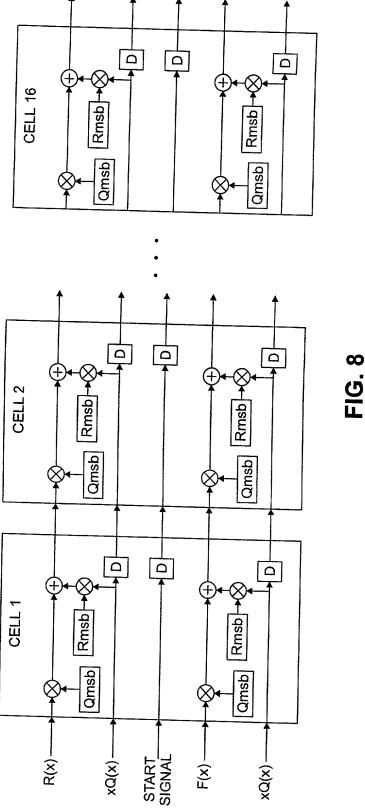
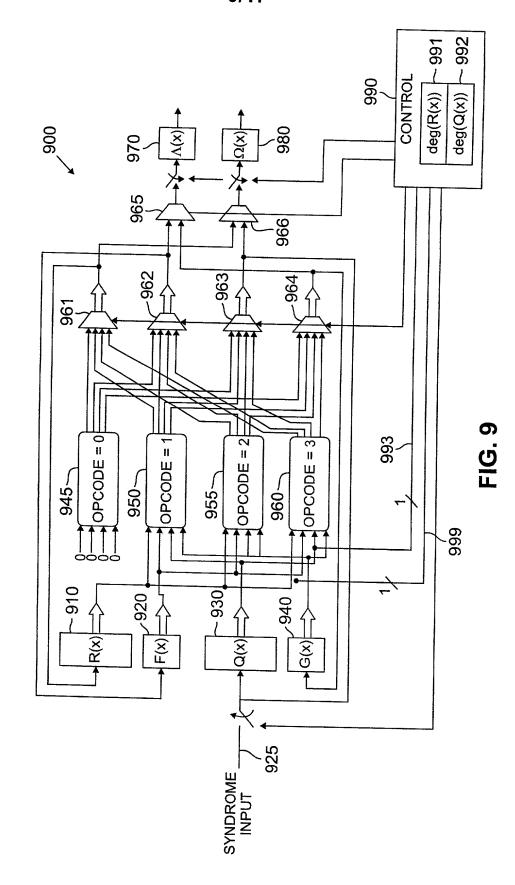


FIG. 7 (PRIOR ART)



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FIG. 8 (PRIOR ART)



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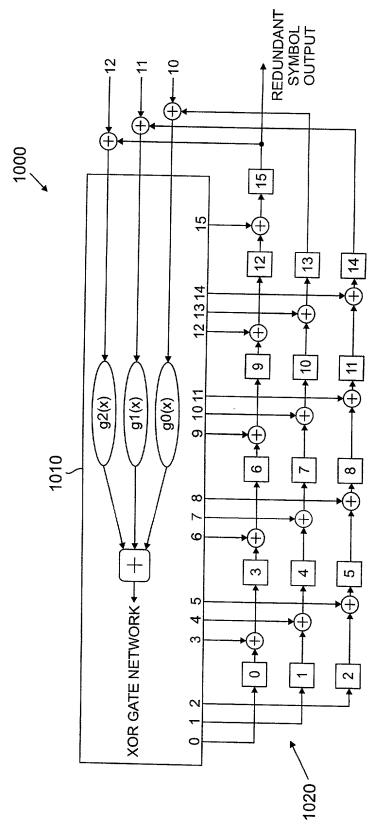


FIG. 10(a)

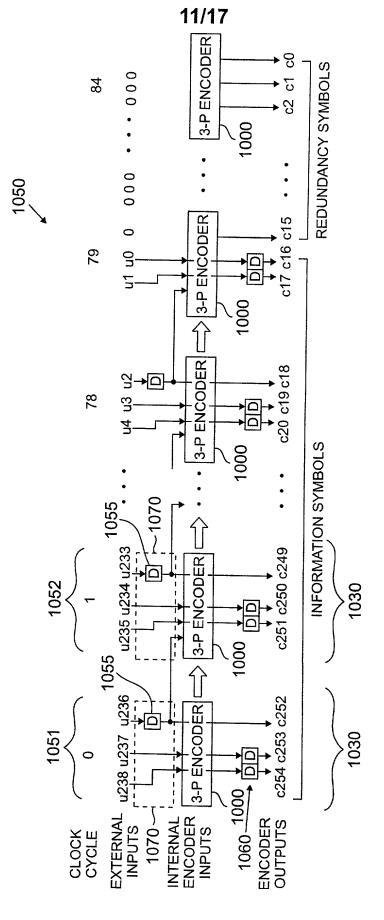


FIG. 10(b)

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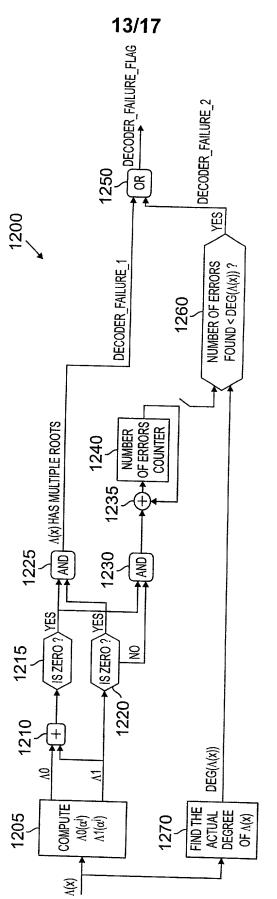


FIG. 12

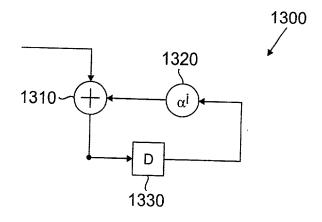


FIG. 13(a)

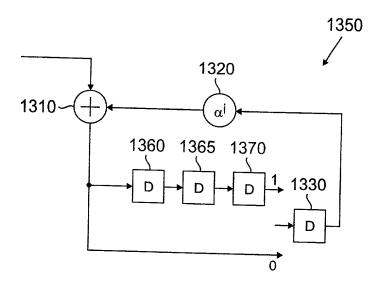


FIG. 13(b)

1400

## 16 BYTES PER CYCLE @ 83 MHz

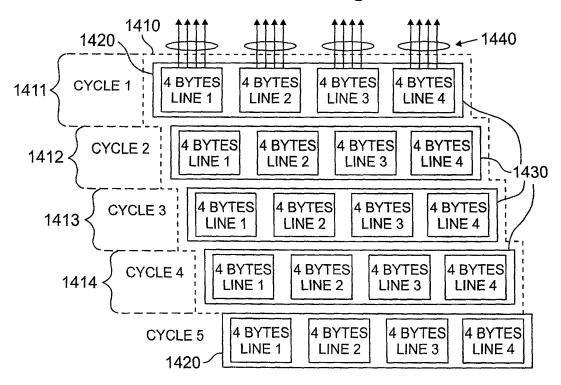
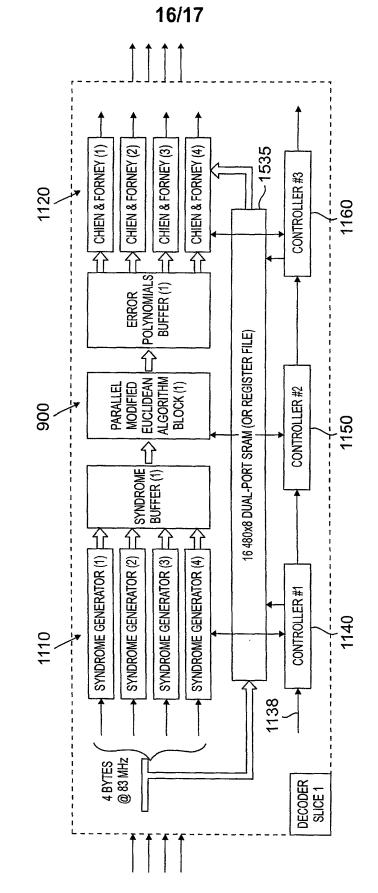


FIG. 14

1500



<u>-16. 15</u>

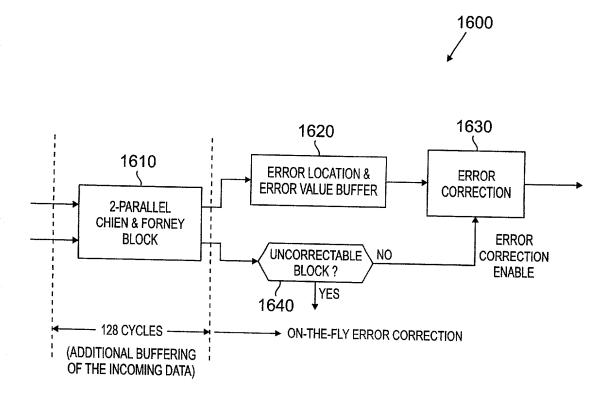


FIG. 16